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Substitute for form 1449A/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Sheet 1 Of 13

Complete if Known	
Application Number	10/569,233
Filing Date	Feb. 22, 2006
First Named Inventor	Bernds
Group Art Unit	Not assigned
Examiner Name	Not assigned
Attorney Docket Number	411000-148

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First Named Inventor	Bernds
Group Art Unit	Not assigned
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Group Art Unit	Not assigned
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Sheet 5 | 13

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Application Number	10/569,233
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First Named Inventor	Bernds
Group Art Unit	Not assigned
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Attorney Docket Number	411000-148

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Application Number	10/569,233
Filing Date	Feb. 22, 2006
First Named Inventor	Bemds
Group Art Unit	Not assigned
Examiner Name	Not assigned
Attorney Docket Number	411000-148

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Sheet 7 of 13

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Application Number	10/569,233
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT				Application Number	10/569,233
				Filing Date	Feb. 22, 2006
				First Named Inventor	Bernds
				Group Art Unit	Not assigned
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Sheet 12 13

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Application Number	10/569,233
Filing Date	Feb. 22, 2006
First Named Inventor	Bernds
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Examiner Name	Not assigned
Attorney Docket Number	411000-148

		ZIE VOOR TITEL BOEK, d 2e PAGINA,XP-002189001, pg 196-228.	

Examiner Signature	/Bernard Lipman/	Date Considered	05/08/2009

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANTS:	Adolf Bernds et al.		
SERIAL NO:	10/569,233		
FILED:	February 22, 2006		
EXAMINER	Not assigned	ART UNIT	Not assigned
FOR:	Polymer Mixtures for Printed Polymer Electronic Circuits		
ATTY DKT NO.:	411000-148	CUSTOMER NO.:	27162

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

DISCLOSURE STATEMENT UNDER 37 CFR 1.56

SIR:

This paper is to bring to the attention of the PTO the following commonly owned copending U.S. applications, all of which are related in different respects to organic electronic devices and/or method of making such devices such as transistors, diodes, integrated circuits and the like. Many of these applications also have one or more common inventors. The attached PTO 1449 lists these applications. It is respectfully requested that the Examiner consider and make of record all of the cited applications listed on the attached PTO 1449.

Application No.	Title	Inventors	Atty. Dkt. No.
10/344,951	Organic Field-Effect Transistor (OFET), A Production Method Therefor, An Integrated Circuit Constructed From the Same and Their Uses	Adolf Bernds et al.	411000-99
10/362,932	Organic Field Effect Transistor, Method for Structuring an OFET and Integrated Circuit	Adolf Bernds et al	411000-110
10/380,113	Organic Rectifier, Circuit, RFID Tag and Use of an Organic Rectifier	Adolf Bernds et al.	411000-106

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10/381,032	Electrode and/or Conductor Track for Organic Components and Production Method Thereof	Adolf Bernds et al.	411000-105
10/433,959	Organic Field Effect Transistor, Method For Structuring an OFET and Integrated Circuit	Adolf Bernds	411000-108
10/433,961	Device For Detecting and/or Transmitting at Least One Environmental Influence, Method for Producing Said Device and Use Thereof	Wolfgang Clemens et al.	411000-111
10/467,636	Organic Field Effect Transistor With a Photostructured Gate Dielectric, Method for the Production and Use Thereof in Organic Electronics	Adolf Bernds et al.	411000-104
10/473,050	Device With At Least Two Organic Electronic Components and Method for Producing the Same	Adolf Bernds et al.	411000-113
10/479,234	Organic Field Effect Transistor, Method for Production and Use Thereof in the Assembly of Integrated Circuits	Adolf Bernds et al.	411000-101
10/479,238	Method For Producing Conductive Structures by Means of Printing Technique, and Active Components Produced Therefrom For Integrated Circuits	Adolf Bernds et al.	411000-100
10/492,922	Insulator for An Organic Electronic Component	Erwann Guillet et al.	411000-115
10/492,923	Electronic Unit, Circuit Design for the Same and Production Method	Wolfgang Clemens et al.	411000-114
10/498,610	Organic Field Effect Transistor with Offset Threshold Voltage and the Use Thereof	Walter Fix et al.	411000-119
10/508,640	Logic Component Comprising Organic Field Effect Transistors	Walter Fix et al.	411000-120
10/508,737	Device and Method for Laser Structuring Functional Polymers and	Adolf Bernds et al.	411000-121
10/517,750	Substrate for an Organic Field Effect Transistor, Use of the Substrate, Method of Increasing the Charge Carrier Mobility and Organic Field Effect Transistor (OFET)	Wolfgang Clemens et al.	411000-122
10/523,216	Electronic Component Comprising Predominantly Organic Functional Materials And A Process For The Production Thereof	Adolf Bernds et al.	411000-123
10/523,487	Electronic Device	Wolfgang Clemens et al.	411000-124
10/524,646	Organic Component for Overvoltage Protection and Associated Circuit	Walter Fix et al.	411000-127
10/533,756	Organic Electronic Component with High-Resolution Structuring and Process for	Wolfgang Clemens et al.	411000-128

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	the Production Thereof		
10/534,678	Measuring Apparatus for Determining an Analyte in a Liquid Sample	Wolfgang Clemens et al.	411000-129
10/535,448	Organic Electronic Component Comprising Semi-Conductive Functional Layer and Method for Producing Said Component	Wolfgang Clemens et al.	411000-131
10/535,449	Organic Electronic Component Comprising the Same Organic Material for at Least Two Functional Layers	Adolf Bernds et al.	411000-132
10/344,926	An Electronic Circuit Having an Encapsulated Organic-Electronic Component, and a Method for Making an Encapsulated Organic-Electronic Component	Wolfgang Clemens et al.	411000-133
10/541,815	Organo-Resistive Memory Unit	Axel Gerlt et al.	411000-136
10/541,956	Board or Substrate for an Organic Electronic Device and Use Thereof	Wolfgang Clemens et al.	411000-137
10/541,957	Organic Field Effect Transistor And Integrated Circuit	Walter Fix et al.	411000-138
10/543,561	Organic Storage Component and Corresponding Triggering Circuit	Wolfgang Clemens et al.	411000-139
10/542,678	Organic Electronic Component and Method For Producing Organic Electronic Devices	Adolf Bernds et al.	411000-140
10/542,679	Use of Conductive Carbon Black/Graphite Mixtures for the Production of Low-Cost Electronics	Adolf Bernds et al.	411000-141
10/562,989	Method and Device for Patterning Organic Layers	Jurgen Ficker	411000-143
10/562,869	Logic Gate With a Potential-Free Gage Electrode for Organic Integrated Circuits	Wolfram Glauert	411000-144
10/569,763	Organic Electronic Component With High Resolution Structuring And Method For The Production Thereof	Walter Fix	411000-146
10/568,730	Organic Capacitor With Voltage-Controlled Capacitance	Wolfgang Clemens	411000-147
10/569,233	Polymer mixtures for printed polymer electronic circuits	Adolf Bernds	411000-148
10/570,571	Mechanical Control Elements For Organic Polymer Electronic Devices	Wolfgang Clemens	411000-149

The Commissioner is authorized to charge payment of any fees associated with this communication or credit any overpayment to Deposit Account No. 03-0678.

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I hereby certify that this correspondence is being deposited on the date set forth below with the U.S. Postal Service as First Class Mail, postage prepaid, in an envelope addressed to:

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Janice Speidel — **May 31, 2006** **Date**

290452v1

Respectfully submitted,
Adolf Bernds et al.

by William Squire, Reg. No. 25,378
Attorney for applicants
CARELLA, BYRNE,BAIN, GILFILLAN,
CECCHI, STEWART & OLSTEIN
5 Becker Farm Road
Roseland, NJ 07068
Tel: (973)994-1700
Fax: (973)994-1744

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Sheet 1 of 2

Complete if Known	
Application Number	10/569,233
Filing Date	February 22, 2006
First Named Inventor	Adolf Bernds
Group Art Unit	Not assigned
Examiner Name	Not assigned
Attorney Docket Number	411000-148

U.S. PATENT DOCUMENTS

Examiner Initial*	Cite No.[1]	Document Number / Number-Kid Code[2] (if known)	Publication- Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
	133	US-10/344,926	02/12/2004	Adolf Bernds et al.	See Disclosure Statements filed
	99	US-10/344,951	02/12/2004	Adolf Bernds et al.	
	106	US-10/380,113	09/25/2003	Adolf Bernds et al.	
	105	US-10/381,032	02/12/2004	Adolf Bernds et al.	
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	111	US-10/433,961	04/01/2004	Wolfgang Clemens et al.	
	109	US-10/451,108	05/13/2004	Mark Giles et al.	
	104	US-10/467,636	11/04/2004	Adolf Bernds et al.	
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	101	US-10/479,234	12/30/2004	Adolf Bernds et al.	
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	115	US-10/492,922	03/03/2005	Erwann Buillet et al.	
	114	US-10/492,923	12/23/2004	Wolfgang Clemens et al.	
	119	US-10/498,610	09/29/2005	Walter Fix et al.	
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	121	US-10/508,737	05/19/2005	Adolf Bernds et al.	
	122	US-10/517,750	10/13/2005	Wolfgang Clemens et al.	
	123	US-10/523,216	02/02/2006	Adolf Bernds et al.	
	124	US-10/523,487	04/13/2006	Wolfgang Clemens et al.	
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	128	US-10/533,756	N/A	Wolfgang Clemens et al.	
	129	US-10/534,678	N/A	Wolfgang Clemens et al.	
	131	US-10/535,448	N/A	W. Clemens et al.	
	132	US-10/535,449	2/16/2006	Walter Fix et al.	

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	133	US-10/344,926	02/12/2004	Adolf Bernds et al.	
	136	US-10/541,815	N/A	Axel Gerlt et al.	
	137	US-10/541,956	N/A	Wolfgang Clemens et al.	
	138	US10/541,957	N/A	Walter Fix et al.	
	139	US-10/543,561	N/A	Wolfgang Clemens et al.	
	140	US-10/542,678	N/A	Adolf Bernds et al.	
	141	US-10/542,679	03/16/2006	Adolf Bernds et al.	
	143	US-10/562,989	N/A	Jurgen Ficker et al.	
	144	US-10/562,869	N/A	Wolfram Glauert	
	146	US-10/569,763	N/A	Walter Fix et al.	
	147	US-10/568,730	N/A	Wolfgang Clemens et al.	
	148	US-10/569,233	N/A	Adolf Bernds et al.	
	149	US-10/570,571	N/A	Clemens et al.	
Examiner Signature		/Bernard Lipman/		Date Considered	05/08/2009

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. 1 Applicant's unique citation designation number (optional). 2 See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. 3 Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). 4 For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. 5 Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. 6 Applicant is to place a check mark here if English language Translation is attached.

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